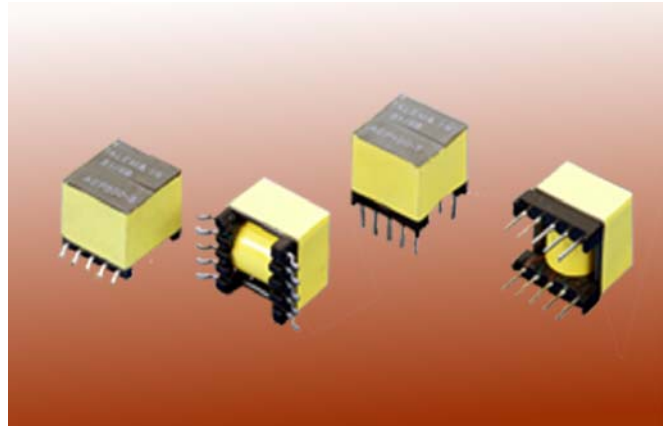


SEP Series • SDSL / SHDSL Transformers for Infineon Chipsets

Features

- Complies with ANSI and ETSI requirements
- Matched to Infineons PEB22622, PEF22623 & PEF24622 chipsets
- Very low THD
- 2000V minimum isolation
- All materials approved to UL94V-0
- Excellent quality at competitive price due to high volume production
- Manufactured in an ISO 9001:2015 and ISO 14001:2015 certified Talema facility
- Fully RoHS & REACH Compliant and meets lead free reflow level J-STD-020F for SMD

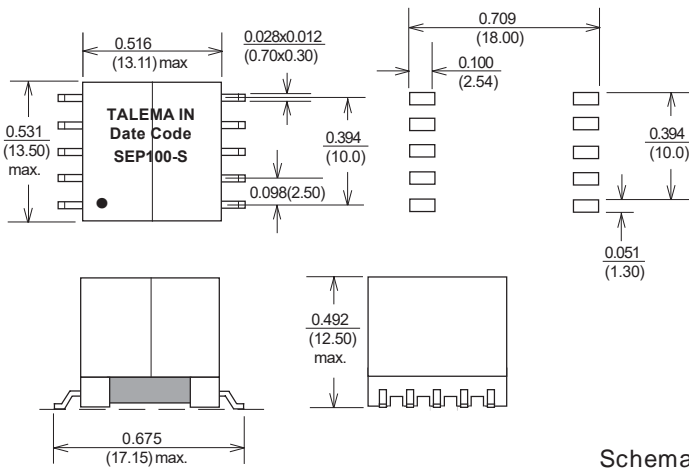


Electrical Specifications @ 25°C

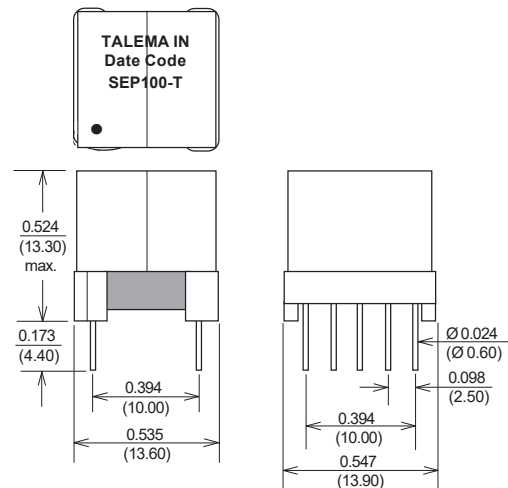
Turns Ratio: **Bold** = IC side windings

Electrical Specifications @ 25°C • Operating Temperature -40° to +85°C							
Part Number	Package	Turns Ratio ±2% Line : IC:Sense	L _p (mH) ±10%	L _L (µH) Max.	DCR (Ohms Max.) Line / IC	DCR (Ohms Max.) Sense Winding	Isolation Voltage (Vrms)
SEP100-T	TH	3.2 : 1 : 1	3.0	25	5.0 / 0.80	3.6	2000
SEP100-S	SMD						

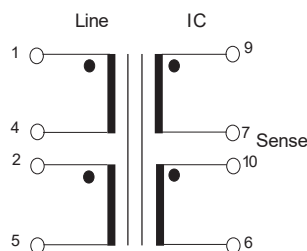
Package "S"



Package "T"



Schematic



Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise
Surface Coplanarity will be 0.004 (0.10) maximum

Regional Locations - Design, Manufacturing, Sales & Marketing

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Locations of Talema Group Regional Offices

